	Туре	L #	Hits	Search Text	DBs	Time Stamp	en	Er ro r De fi ni ti	Er ro rs
1	BRS	L2		plug with wiring with simultaneous\$	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 23:11			
2	BRS	L3		plug with wiring with simultaneous\$ with inter\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 20:47			
3	BRS	L4	0	plug with wiring with simultaneous\$ and mcm	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 20:47			
4	BRS	L5	I .	plug with wir\$4 with simultaneous\$ and mcm	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 20:47			
5	BRS	L6	ı	plug near wir\$4 with simultaneous\$	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 20:48			
6	BRS	L7	81	plug near wir\$4 with (simultaneous\$ or "same step")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 20:49			

	Туре	L #	Hits	Search Text	DBs	Time Stamp	en	Er ro r De fi ni ti	Er ro rs
7	BRS	L8		plug near wir\$4 with form\$4 near (simultaneous\$ or "same step")	USPAT; EPO; JPO; DERWENT;	2005/07/20 20:55			
8	BRS	L9	19	plug with wir\$4 with form\$4 with (simultaneous\$ or "same step") and stack\$4 and interlayer adj dielectric	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 20:59			
9	IS&R	L10	625	(438/666).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 21:21			
10	BRS	L11		("438"/\$).ccls. and multichip near stack	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 21:34			

	Туре	L #	Hits	Search Text	DBs	Time Stamp	en	Er ro r De fi ni ti	Er ro rs
11	BRS	L12	41	("4638552" "4794092" "4930216" "4978639" "5107586" "5217916" "5275958" "5314844" "5380681" "5466634").PN. OR ("5571754").URP N.	US-PGPUB; USPAT; USOCR				
12	BRS	L13	19	("5241456" "5281852" "5324687" "5353498" "5466634" "5517754"	US-PGPUB; USPAT; USOCR	2005/07/20 21:31			
13	BRS	L15	1332	integrated adj circuit and (connect\$5 or	h P() *	2005/07/20 22:59			

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	Туре	L#	Hits	Search Text	DBs	Time Stamp	Co mm en ts	De fi	Er ro rs
14	BRS	L14	53	multichip near stack and integrated adj circuit and (connect\$5 or interconnect\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 21:39			
15	BRS	L16	2157	mcm with (connect\$5 or interconnect\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 21:40			
16	BRS	L17	0	mcm with (connect\$5 or interconnect\$5) and plug with wiring with simultaneous\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 21:41			
17	BRS	L18	246	(connect\$5 or interconnect\$5) and plug with wiring with simultaneous\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 21:41			
18	BRS	L19	462	and plug with wir\$4 with	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 21:42			

	Туре	L #	Hits	Search Text	DBs	Time Stamp	l	rs
19	BRS	L20	11	(connect\$5 or interconnect\$5) and plug with wir\$4 with simultaneous\$4 and wir\$4 with length with width	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 21:44		
20	BRS	L21	38	mcm and (connect\$5 or interconnect\$5) and wir\$4 with length with width	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 22:07		
21	IS&R	L22	2273		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 22:07		
22	BRS	L23	7	interconnect\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 23:08		

	Туре	L #	Hits		DBs	Time Stamp	1	 Er ro rs
23	BRS	L24	0	\$5chip near stack\$4 and integrated adj circuit and (connect\$5 or interconnect\$5) and via near length with width	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 23:08		
24	BRS	L25	6	\$5chip near stack\$4 and integrated adj circuit and (connect\$5 or interconnect\$5) and via with length with width	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 23:08		
25	BRS	L26	10	plug with wiring with simultaneous\$ and via with (dimension or length with width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/07/20 23:12		

	Type	L #	Hits	Search Text	DBs	Time Stamp	en	Er ro r De fi ni ti on	Er ro rs
26	BRS	L27		1" 7471743" 1	US-PGPUB; USPAT; USOCR	2005/07/20 23:15			
27	BRS	L28	343	("2933437" "3911562" "4305779" "4366613" "4475981"	US-PGPUB; USPAT; USOCR	2005/07/20 23:20			